

IN THE CLAIMS:

1. (Presently Amended) A liquid crystal display comprising a TFT substrate, a counter substrate facing ~~to~~ the TFT substrate, and liquid crystal interposed between the substrates, wherein the TFT array substrate has a display area and a terminal forming area,

the display area is provided with a pixel electrode, a switching element connected to the pixel electrode, a gate line connected to the switching element and a source line connected to the switching element,

the terminal forming area is provided with a terminal electrode for connecting the gate line or source line to at least one external signal source, and

wherein

a first metallic line and a second metallic line, both connected to the terminal electrode via respective contact holes, are arranged below the terminal electrode at the terminal forming area, and an insulating layer is interposed between the first metallic line and the second metallic line and serves, during fabrication of said display, to minimize exfoliation of the second metallic line, and short circuits resulting from such exfoliation.

2. (Original) A liquid crystal display according to claim 1,
wherein

the first metallic line is made from the same layer as that
for the source line and

the second metallic line is made from the same layer as that
for the gate line.

3. (Original) A liquid crystal display according to
claim 1, wherein

the first metallic line is connected to the source line and
the second metallic line is arranged in a lower layer than
the first metallic line.

4. (Original) A liquid crystal display according to claim 2,
wherein

the first metallic line is connected to the source line and
the second metallic line is arranged in a lower layer than
the first metallic line.

5. (Original) A liquid crystal display according to claim 1, in which

the second metallic line is connected to the gate line and the second metallic line is arranged in a lower layer than the first metallic line.

6. (Original) A liquid crystal display according to claim 2, in which

the second metallic line is connected to the gate line and the second metallic line is arranged in a lower layer than the first metallic line.